

EAST - [Untitled1:1]

FileViewEditToolsWindowHelp

Active

L1: (131) 708/252.ccls.

L2: (70) 708/253,256.ccls.

L3: (15) 1 and 2

L4: (238962) programmable or reconfigurable

L6: (396279) feedback or feed adj back

L7: (4043) lfsr or (6 adj shift\$3)

L8: (14024) clb pla fpla

L9: (246971) 4 or 8

L10: (120) 7 with 9

L11: (118) 4 and 10

L13: (42465) input adj (mux or multiplex\$3 or select\$3)

L14: (748) 9 with 13

L15: (4) 11 and 14

L16: (33) 11 and 13 not 14

L17: (83) 10 not (15 16)

L18: (17) 7.ti. and 17

L19: (32) 7.ab. and 17 not 18

L20: (34) 17 not (18 19)

Failed

(0) lfsr or (5 adj shift\$3)

(30976) input adj (mux or multiplexor\$1 or select\$)

Saved

Favorites

Search

Qbs

USPAT,US-PGPUB,EPO,JPO,DERWENT,IBM,TDOS

Default operator

OR

Highlight all hit terms initially

IRS form

SAR form

Image

Text

HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval CI	Inventor	S	C	P	3	In
1			US 6516432 B1	20030204	14	AC scan diagnostic method	714/732	714/728; 714/728;		Motika, Franco et al.	P				US
2			US 5737422 A	19980407	9	Distributed data processing network	713/159			Billings, Roger E.	P				US
3			US 4536881 A	19850820	12	Integrated logic circuit adapted to performance tests	377/70	377/72; 377/81		Kasuya, Yoshihiro	P				US
4			US 6061417 A	20000509	24	Programmable shift register	377/26	377/69; 377/80;		Kelem, Steven H.	P				US
5			US 6014408 A	20000111	15	PN code generating circuit and terminal unit	375/130	341/187; 708/250;		Naruse, Tetsuya et al.	P				US
6			US 6181164 B1	20010130	20	Linear feedback shift register in a programmable gate array	326/46	326/40; 326/41		Miller, Andrew J.	P				US

Ready

NUM